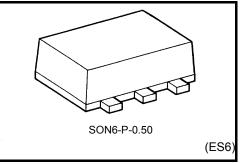
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PG04AFE

Dual Inverter

Features

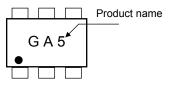
- High-level output current: I_{OH}/I_{OL} = ±8 mA (min)
 - at V_{CC} = 3 V
- High-speed operation: t_{pd} = 2.8 ns (typ.)
 - at V_{CC} = 3.3 V, 15pF
- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.5-V tolerant inputs

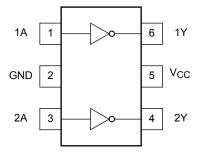


Weight: 0.003 g (typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	VIN	-0.5~7.0	V
DC output voltage	VOUT	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20 (Note 1)	mA
DC output current	IOUT	±25	mA
DC V _{CC} /GND current	ICC	±100	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65~150	°C

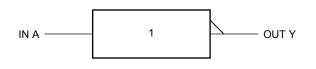
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

<u>TOSHIBA</u>

IEC Logic Symbol



А	Y
L	Н
Н	L

Operating Range

Characteristics	Symbol	Value	Unit		
Power supply voltage	V _{CC}	0.9~3.6	V		
Input voltage	V _{IN}	0~5.5	V		
Output voltage	V _{OUT}	0~V _{CC} (Note 2)	V		
Output Current	IOH/IOL	±8.0 (Note 3)			
		±4.0 (Note 4)			
		±3.0 (Note 5)	~ ^		
		±1.7 (Note 6)	mA		
		±0.3 (Note 7)			
		±0.02 (Note 8)			
Operating temperature	T _{opr}	-40~85	°C		
Input rise and fall time	dt/dV	0~10 (Note 9)	ns/V		

- Note 2: $V_{CC} = 3.0 \sim 3.6 \text{ V}$
- Note 3: $V_{CC} = 2.3 \sim 2.7 \text{ V}$
- Note 4: $V_{CC} = 1.65 \sim 1.95 \text{ V}$
- Note 5: $V_{CC} = 1.4 \sim 1.6 V$
- Note 6: $V_{CC} = 1.1 \sim 1.3 V$
- Note 7: $V_{CC} = 0.9 V$
- Note 8: V_{IN} = 0.8~2.0 V, V_{CC} = 3.0 V

Electrical Characteristics

DC Electrical Characteristics

Characteristics	Symbol	ymbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		1631	V _{CC} (V		Min	Тур.	Max	Min	Max	Unit
High-level VIH input voltage			0.9	V _{CC}		_	V _{CC}	—		
			1.1~1.3	V _{CC} × 0.7			V _{CC} × 0.7		V	
			1.4~1.6	V _{CC} × 0.65			V _{CC} × 0.65			
			1.65~1.95	V _{CC} × 0.65		_	V _{CC} × 0.65			
				1.7	_	_	1.7	_		
				3.0~3.6	2.0		_	2.0	_	
					_		GND	_	GND	
						$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$		$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$		
Low-level	VIL		_				$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$		$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	V
input voltage			1.65~1.95			V _{CC} × 0.35		V _{CC} × 0.35		
			2.3~2.7			0.7		0.7		
				3.0~3.6			0.8		0.8	
		VIN = VIL	I _{OH} =-0.02 mA	0.9	0.75	_		0.75	—	V
			I _{OH} = -0.3 mA	1.1~1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		—	V _{CC} × 0.75	_	
High-level V _{OH}	Vон		$I_{OH} = -1.7 \text{ mA}$	1.4~1.6	V _{CC} × 0.75		_	V _{CC} × 0.75		
			I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
			I _{OH} = -4.0 mA	2.3~2.7	2.0	_		2.0	_	
			I _{OH} = -8.0 mA	3.0~3.6	2.48	_	_	2.48	_	
			$I_{OL} = 0.02 \text{ mA}$	0.9			0.1	_	0.1	
Low-level V _{OL}	V _{IN} = V _{IH}	I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25	—	V _{CC} × 0.25	V	
		I _{OL} = 1.7 mA	1.4~1.6	_	_	V _{CC} × 0.25	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
			I _{OL} = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
		I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	1	
			I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage current	I _{IN}	V _{IN} = 0~5.5V		0~3.6	_	_	±0.1		±1.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		3.6			1.0		10.0	μΑ

AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = −40~85°C		Unit	
Syn	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		27.2		_	—	
			1.1~1.3	_	12.2	23.2	1.0	42.6	
			1.4~1.6	_	6.5	10.2	1.0	12.0	
			1.65~ 1.95	_	4.7	7.0	1.0	7.6	
			2.3~2.7		3.1	4.4	1.0	4.9	
	t _{pLH} t _{pHL}		3.0~3.6		2.4	3.5	1.0	4.1	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		29.8		_	—	
			1.1~1.3		13.5	26.0	1.0	44.5	- ns -
			1.4~1.6		7.2	11.4	1.0	13.6	
			1.65~ 1.95		5.2	7.5	1.0	7.7	
			2.3~2.7		3.4	4.8	1.0	5.5	
			3.0~3.6		2.8	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		40.7		_	—	
			1.1~1.3	_	17.8	33.9	1.0	64.1	
			1.4~1.6		9.1	14.3	1.0	17.4	
			1.65~ 1.95		6.6	9.8	1.0	10.2	
			2.3~2.7		4.1	6.2	1.0	6.6	
			3.0~3.6		3.3	4.8	1.0	5.2	
Input capacitance	C _{IN}		3.6		3			_	pF
Power dissipation capacitance	C _{PD}	(Note 9)	0.9 ~ 3.6	—	6	—	_	—	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

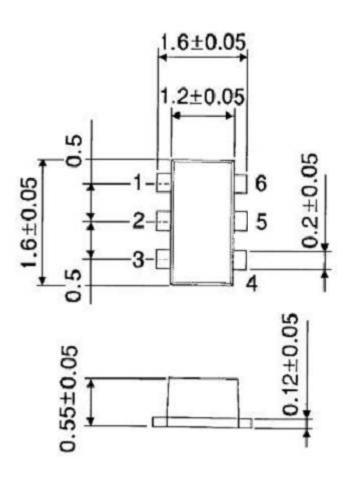
Average operating current can be obtained by the equation:

 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Package Dimensions

SON6-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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20070701-EN GENERAL

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